SPECIFICATIONS

Modulation Input

Input Impedance
Analog Input (SMB Male)

RF Output

Center Frequency (Fc)

Output Power (SMA Female)

Rise/Fall Time

RF Contrast Ratio

Harmonic Distortion

Output Impedance

Output VSWR

Modulation Bandwidth

Power Supply Voltage (+Vcc)

ALC Voltage Level

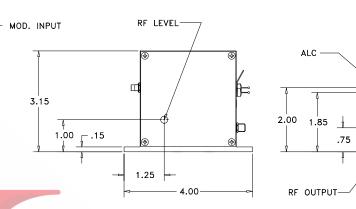
ALC Bandwidth

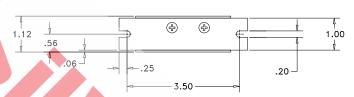


35 kHz

2.00

OUTLINE DRAWING





Notes:

- 1. Output power factory set to 2.5 W at +1.0 V input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
- 2. When calculating the contrast ratio, it is understood that only the power of the 300 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
- 3. A +25 V nominal input on the ALC corresponds to full RF output power. The RF output will be switched off with an ALC voltage of less than +18 Volts. Full RF power occurs if ALC input is left unconnected.

Document

+Vcc

.81

10/16/13

Control

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TOLERANCES: .XX ± .01 .XXX ± .005	DR	G. Scholz 10/14/2013	Gooch & Housego			
ROHS FINISH: Compliant	CHK		AODR 1300AF-AIF0-2.5			
	APP		PART NUMBER: 97-03307-47	7	REV:	1 of 1