

SPECIFICATIONS

Modulation Input

Input Impedance
Analog Input (SMC)

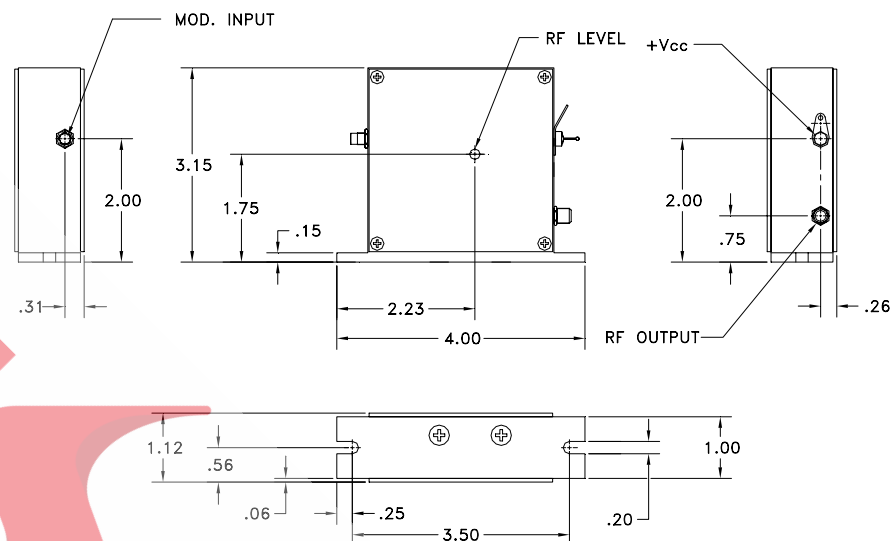
50 Ohms
0 to +1.0 VDC

RF Output

Center Frequency (Fc)
Output Power (SMA Female)
Rise/Fall Time
RF Contrast Ratio
Harmonic Distortion
Output Impedance
Output VSWR
Power Supply Voltage

150 MHz $\pm 0.1\%$
2.5 W
7 nsec Max
50 dB min
-20 dBc
50 Ohms
1.5 : 1 Max
+24 V @ 775 mA

OUTLINE DRAWING



Notes:

1. Output power factory set to 2.5 W at 1.0 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0 - 40° C, after 5 minute warm-up.
2. When calculating the contrast ratio, it is understood that only the power of the 150 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
3. RoHS compliant.

Document

11/10/11

Control

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TOLERANCES: .XX $\pm .01$.XXX $\pm .005$	DR	T. Moon 10/27/2011	Crystal Technology, LLC		
MATERIAL:	CHK		DESCRIPTION: AODR 1150AF-AINA-2.5 HCR		
FINISH:	APP				
	APP		PART NUMBER: 97-02910-14	REV: 1	1 of 1