

## SPECIFICATIONS

### Modulation Input

Input Impedance  
Digital Input (SMC)

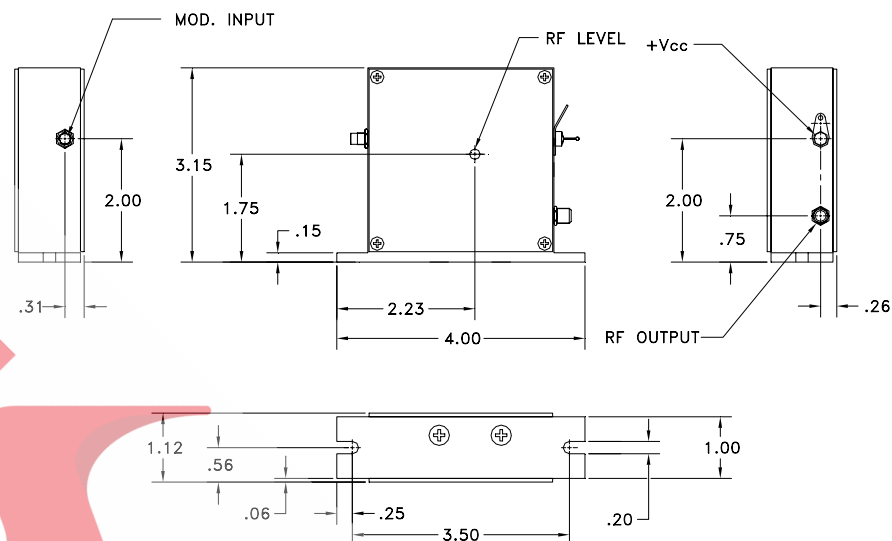
75 Ohms  
Standard TTL

### RF Output

Center Frequency (Fc)  
Output Power (SMA Female)  
Rise/Fall Time  
RF Contrast Ratio  
Harmonic Distortion  
Output Impedance  
Output VSWR  
Power Supply Voltage

80 MHz  $\pm 0.1\%$   
3.0 W  
12 nsec Typ.  
50 dB min  
-20 dBc  
50 Ohms  
1.5 : 1 Max  
+24 V @ 775 mA

## OUTLINE DRAWING



### Notes:

- Output power factory set to 3.0 W at 2.4 Volt input. Power stability less than 5% over the heat sink's ambient temperature range of 0 - 40° C, after 5 minute warm-up.
- When calculating the contrast ratio, it is understood that only the power of the 80 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
- RoHS compliant

Document

01/12/12

Control

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TOLERANCES: .XX $\pm .01$ .XXX $\pm .005$	DR	Geri Scholz 1/9/2012	Crystal Technology, LLC		
MATERIAL:	CHK		DESCRIPTION: <b>AODR 1080AF-DINA-3.0 HCR</b>		
FINISH:	APP				
	APP		PART NUMBER: <b>97-02910-19</b>	REV: <b>1</b>	1 of 1