

SPECIFICATIONS

OUTLINE DRAWING

Modulation Input

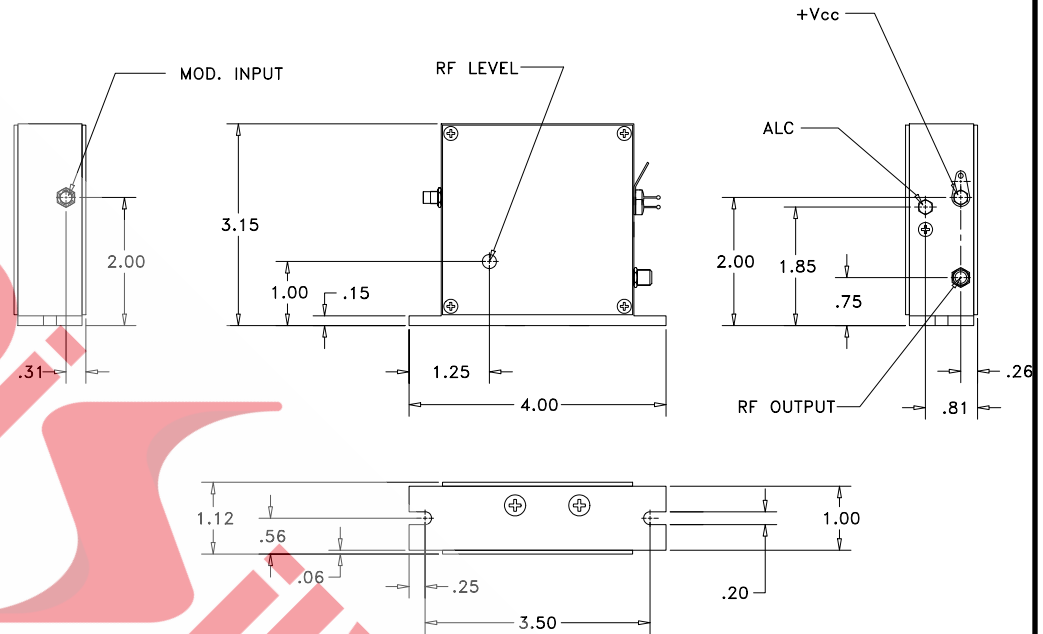
Input Impedance
Analog Input (SMB Male)

50 Ohms
Standard TTL

RF Output

Center Frequency (Fc)
Output Power (SMA Female)
Rise/Fall Time
RF Contrast Ratio
Harmonic Distortion
Output Impedance
Output VSWR
Modulation Bandwidth
Power Supply Voltage (+Vcc)
ALC Voltage Level
ALC Bandwidth

300 MHz \pm 0.1%
2.5W
3 nsec Typ.
35 dB min
-20 dBc
50 Ohms
1.5 : 1 Max
150 MHz
+28 V @ 700 mA
+20 to +25 V nominal
35 kHz



Notes:

1. Output power factory set to 2.5 W at a 2.4 V, 30 mA input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
2. When calculating the contrast ratio, it is understood that only the power of the 300 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
3. A +25 V nominal input on the ALC corresponds to full RF output power. The RF output will be switched off with an ALC voltage of less than +18 Volts. Full RF power occurs if ALC input is left unconnected.

Document
10/16/13
Control

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TOLERANCES: .XX \pm .01 .XXX \pm .005	DR	G. Scholz 10/14/2013		
MATERIAL: 	CHK		DESCRIPTION: AODR 1300AF-DIF0-2.5	
FINISH: 	APP		PART NUMBER: 97-03307-46	REV: A
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